



**Synway CTI Series**

**SHD-120/240D-CT/PCI  
SHD-120/240D-CT/PCI/EC  
SHD-120D-CT/PCI/MAP**

**Digital Trunk Voice Board**

# **Hardware Manual**

**Version 2.1**

**Synway Information Engineering Co., Ltd**

**[www.synway.net](http://www.synway.net)**

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## Revision History

| Version     | Date    | Comments  |
|-------------|---------|---|
| Version 1.0 | 2006-12 | Initial publication.  |
| Version 2.0 | 2009-3  | Significant revision.   |
| Version 2.1 | 2013-9  | Add relevant content about a new board model SHD-120D-CT/PCI/MAP. |

**Note:** Please visit our website <http://www.synway.net> to obtain the latest version of this document.

# Chapter 1 Overview

The CTI Series SHD-120/240D-CT/PCI, SHD-120/240D-CT/PCI/EC and SHD-120D-CT/PCI/MAP are digital trunk voice boards with PCI bus, and have almost all functions needed for call/voice processing systems that connect to either E1 or T1 trunks. They are designed with enhanced capability in echo cancellation and support DMA read and write operations which provide high transmission rate and require low CPU cost, further improving the system performance.

## 1.1 Functions

- A single board accommodates 4 or 8 E1/T1 trunks
- Supports China SS1, SS7 and ISDN connections in both E1 and T1 modes
- Supports phone-calling and voice-processing functions
- Activity/silence detection
- Automatic Gain Control (AGC) support in recording operation
- Enhanced capability in echo cancellation
- Allows DTMF transmission and detection during voice recording or playback
- The SHD-120D-CT/PCI/MAP board supports three versions of GSM MAP protocol: Phase1, Phase2 and Phase2+. A single board can manage up to 20 signaling links
- Includes H.100 bus, compatible with MVIP, SC and ST buses, facilitating smooth connectivity to third-party boards with H.100 bus for the transfer of acquired voice signals to other devices
- The flexible distributed conferencing system sets no limit on the number of simultaneous conferences and participants in each conference, allows monitoring and recording of the whole conference and each individual speaker
- The on-board lightning-proof circuit reaches the telecom standard and surely eliminates the damage caused by lightning
- Equipped with the EMI circuit, effectively preventing the electromagnetic interference
- Each board has a unique hardware serial number written in the firmware to distinguish itself from other boards and prevent piracy. The number is available via an easy function call with applications
- The on-board authorization code identification circuit is designed for software safety. Users can apply to our company for the authorization code

- Compatible with other series of voice boards from Synway

## 1.2 Features

- **PCI 2.1 Bus Support**

Includes PCI 2.1 bus and uses the universal PCI design supporting 3.3V and 5V PCI slots as well as the PCI-X slot.

- **DMA Read and Write**

Uses the DMA technology based on PCI bus to read and write data, greatly reducing the CPU cost.

- **E1/T1 Support**

Provides an easy selection of the E1 or T1 trunk and its matching impedance via software reconfiguration, not requiring any change in the hardware.

- **Signaling Interface**

SS1 provides two levels of interfaces respectively for MFC transmission/receipt and SS1 connection; SS7 provides two levels of interfaces respectively for MTP and TUP/ISUP, meeting various customer requirements.

- **Signaling Processing**

Installed with loadable signaling processing module, each board supports SS1, SS7 and ISDN, eliminating the need for extra signaling boards. The signaling processing program can be upgraded via a simple software configuration, without having to change the hardware.

- **Signaling Links**

Each board supports up to 8 signaling links and the signaling hot-backup feature, i.e. the signaling can be processed by the standby server whenever something is wrong with the links being used, which increases the flexibility and reliability in a great extent. All the timeslots ranging from 1 to 31, not only TS16, can be used for SS7 and ISDN signalings.

- **Programmable Tone Detector**

Detects single or dual tones at any frequency, offering facility for use with a variety of PBXes and key telephone systems.

- **Software-based Terminal Matching**

A same board supports 4 kinds of trunks which differ in impedance: 100Ω T1 twisted-pair cable, 110Ω J1 twisted-pair cable, 120Ω E1 twisted-pair cable and 75Ω E1 coaxial cable, each of which can be used based on a proper software configuration. What's more, both the transmit and receive terminals are specified via software configurations. As a result, the board is enabled to connect with a

variety of digital trunks and optical transceivers without the need for extra hardware devices.

- **Specialized Driver Algorithm**

The driver uses SPECdial - a specialized driver algorithm - to perform a complete automatic dial process through digital lines and to accurately identify the called-party status.

- **Echo Cancellation**

The self-adaptive echo cancellation feature effectively eliminates echoes under various conditions, which cancels out the effect of voice playback on DTMF and busy tone detection, avoids self-excited oscillation and howling, and minimizes the possibility of registering wrong DTMF and busy tones in a conference call. The SHD-120/240D-CT/PCI/EC board has an increased capability in echo cancellation and offers a much better effect in this aspect.

- **Barge in**

Supports the Barge-in feature.

- **Various CODECs Support**

Offers a large selection of voice CODECs, including hardware-based A-law (G.711),  $\mu$ -law, IMA-ADPCM and software-based 16-bit linear PCM, MP3.

- **Supports WAV File**

The recorded voice files can be edited and played by audio tools such as Cooledit.

- **Synway's Unified SynCTI Driver Development Platform**

Synway owns the intellectual property rights for the unified high-intelligence SynCTI driver development platform. Each system supports up to 2048 channels. The complex call procedures can be analyzed and controlled through simple function calls on the driver platform, without having to understand details.

### 1.3 Operation Principle

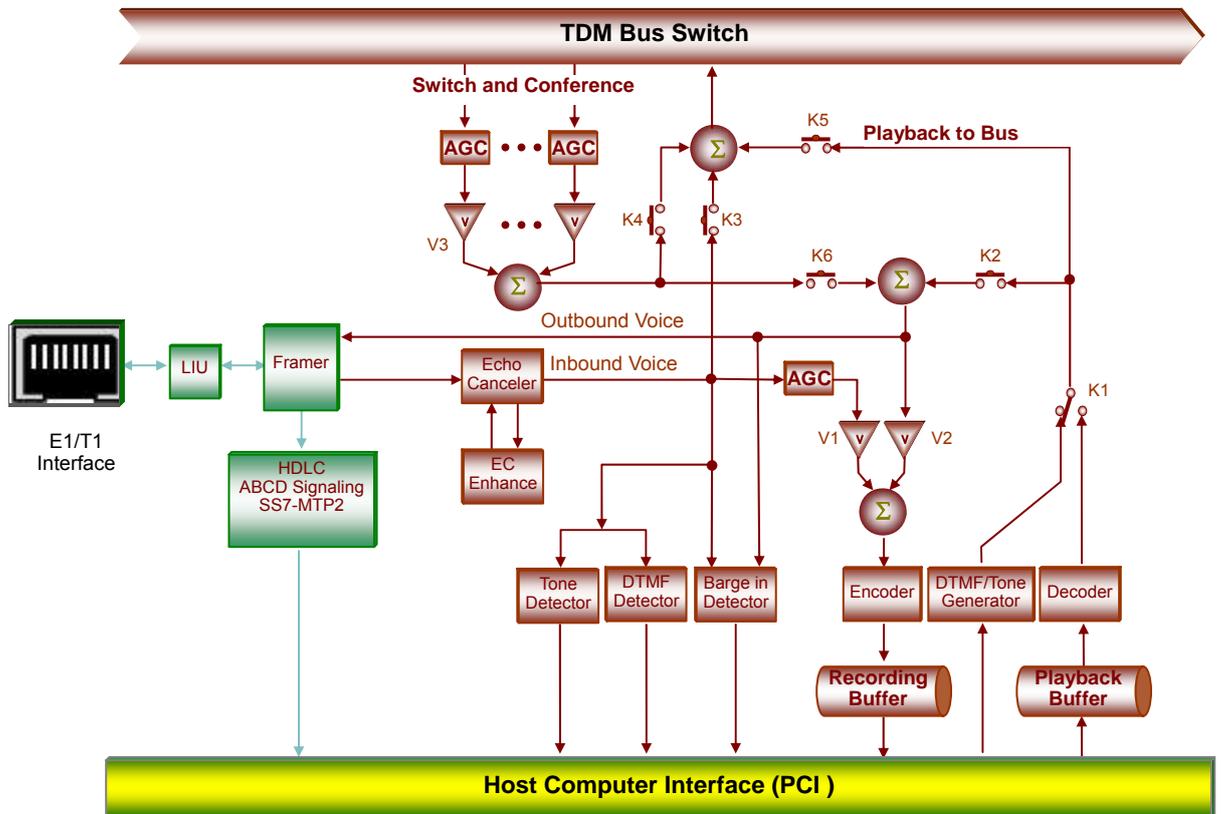


Figure 1-1 Operation Principle of SHD-120/240D-CT/PCI/EC Board

# Chapter 2 Installation

## 2.1 Hardware Structure

- SHD-120D-CT/PCI, SHD-120D-CT/PCI/MAP boards

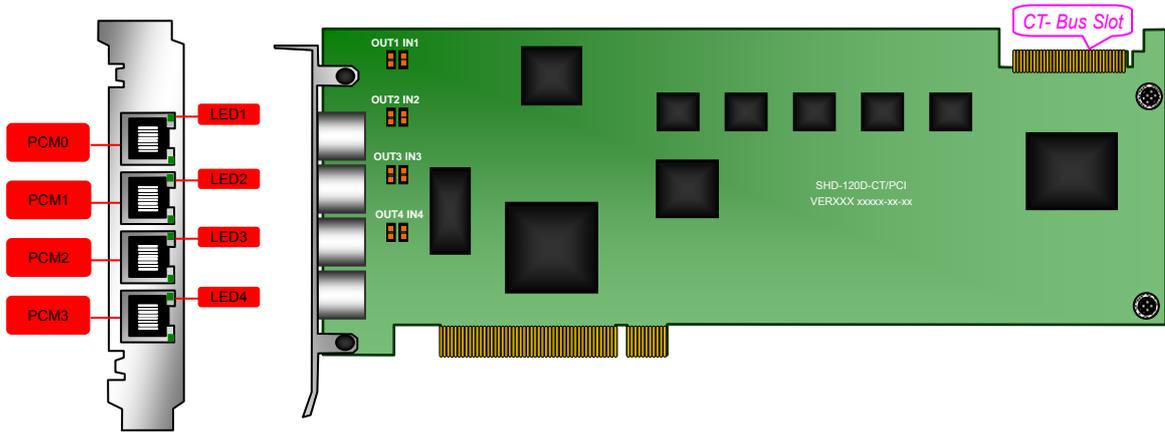


Figure 2-1 Left and Front Side Views

- SHD-120D-CT/PCI/EC board

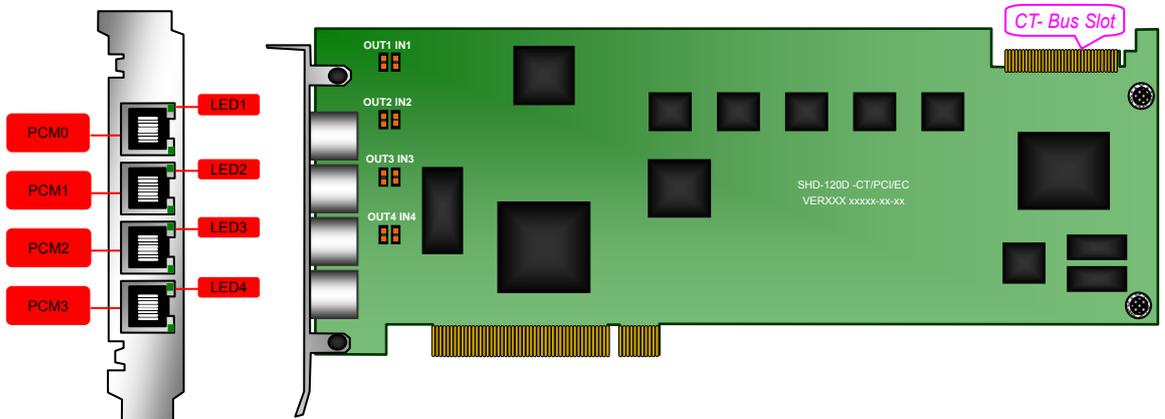


Figure 2-2 Left and Front Side Views

**Notes:** In Figure 2-1 and Figure 2-2, PCM0~PCM3 respectively indicate the 4 trunk interfaces while LED1~LED4 represent their synchronization indicators; IN<sub>m</sub> and OUT<sub>m</sub> refer to the grounding jumpers respectively at the receiving and transmitting ends for PCM(m-1), m=1~4.

● SHD-240D-CT/PCI board

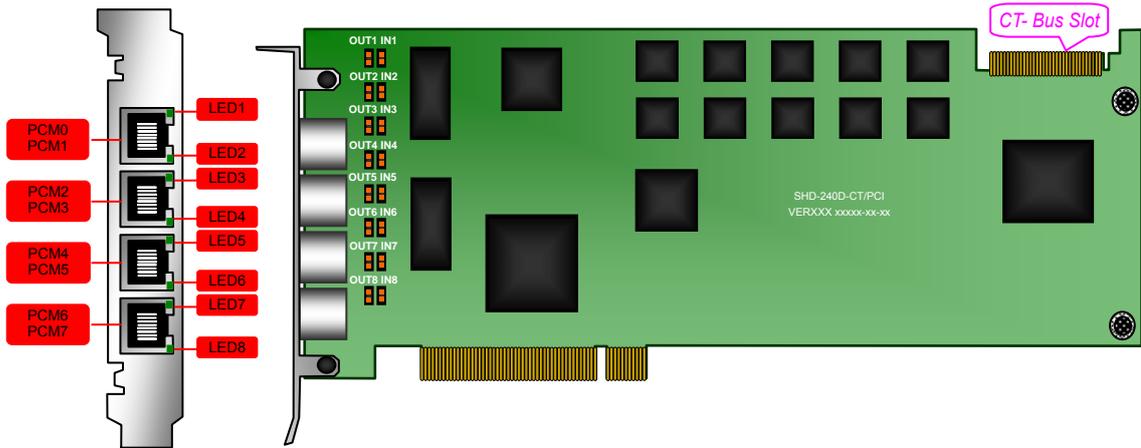


Figure 2-3 Left and Front Side Views

● SHD-240D-CT/PCI/EC board

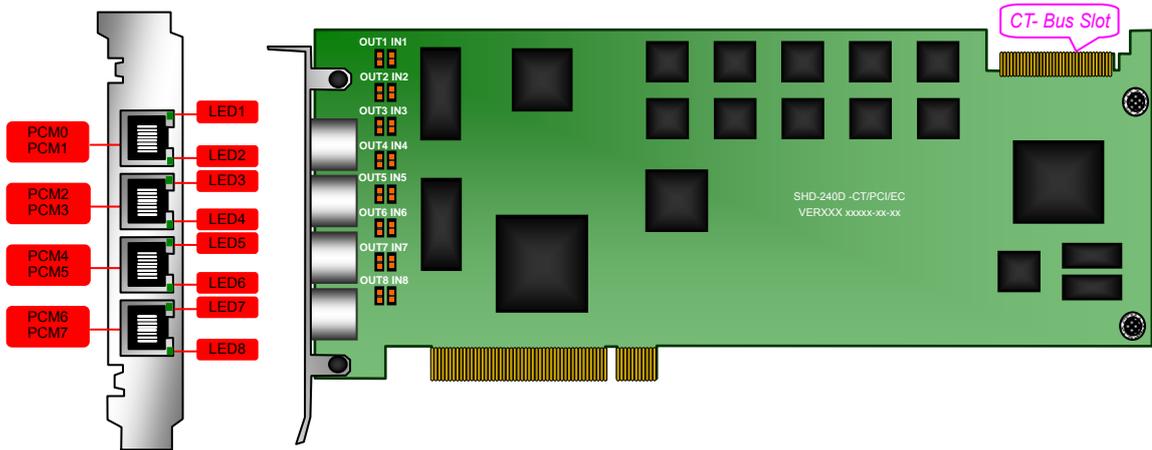


Figure 2-4 Left and Front Side Views

**Notes:** In Figure 2-3 and Figure 2-4, PCM0~PCM7 respectively indicate the 8 trunk interfaces while LED1~LED8 represent their synchronization indicators; IN<sub>m</sub> and OUT<sub>m</sub> refer to the grounding jumpers respectively at the receiving and transmitting ends for PCM(m-1), m=1~8.

The table below describes the synchronization indicators illustrated above.

| LED         | Definition | Lamp Status | Implication              |
|-------------|------------|-------------|--------------------------|
| Green Lamps | Sync       | ON          | synchronous              |
|             |            | OFF         | asynchronous             |
|             |            | FLASH       | synchronous but unsteady |

Table 2-1 On-board Synchronization Indicators

- SHD-120/240D-CT/PCI, SHD-120/240D-CT/PCI/EC, SHD-120D-CT/PCI/MAP boards

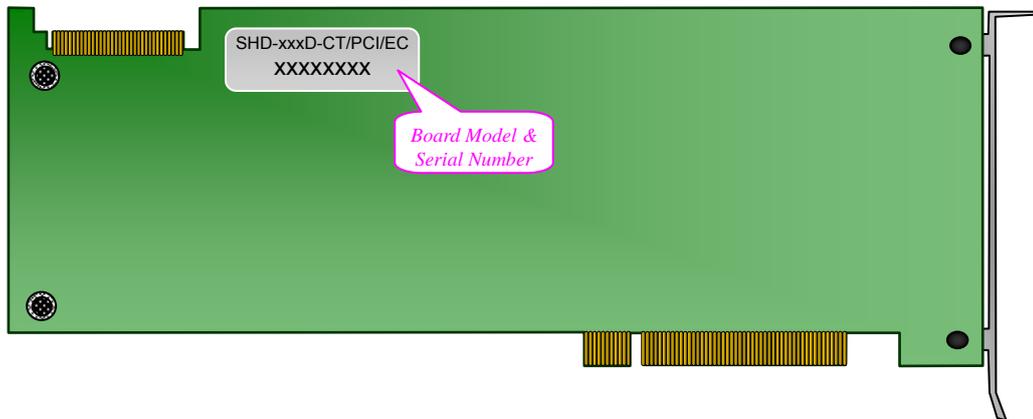


Figure 2-5 Reverse Side View

**Notes:** This file only illustrates four board models with above figures but is also applicable to some other models which have similar hardware structure as listed below in Table 2-2. Always check the label on the board to get the board model. Interfaces on these boards are all RJ48C connectors. Users may convert them into BNC connectors by using the RJ48C-to-BNC adapter supplied with the board.

| No. | Model               | Trunk   | Physical Interface           |
|-----|---------------------|---------|------------------------------|
| 1   | SHD-120D-CT/PCI     | 4 E1/T1 | 4 RJ48C (1 E1/T1 per RJ48C ) |
| 2   | SHD-120D-CT/PCI/ EC | 4 E1/T1 | 4 RJ48C (1 E1/T1 per RJ48C ) |
| 3   | SHD-240D-CT/PCI     | 8 E1/T1 | 4 RJ48C (2 E1/T1 per RJ48C ) |
| 4   | SHD-240D-CT/PCI/EC  | 8 E1/T1 | 4 RJ48C (2 E1/T1 per RJ48C ) |
| 5   | SHD-120D-CT/PCI/MAP | 4 E1/T1 | 4 RJ48C (1 E1/T1 per RJ48C ) |

Table 2-2 Board Model List

## 2.2 System Requirements

### Host System Requirements

CPU: 300MHz Intel® Pentium® II or above

Memory: 256M or more

HD: Depends on individual requirements

### Supported Operating Systems

Refer to *SynCTI Programmer's Manual.pdf*.

## 2.3 Installation Procedure

### Step 1: Configure the grounding jumper

Disconnect all grounding jumpers in the use of the 100Ω T1 twisted-pair cable, the 110Ω J1 twisted-pair cable or the 120Ω E1 twisted-pair cable.

In consideration of various line conditions, this series boards are equipped with two groups of grounding jumpers on each channel which respectively control the groundings of the transmitting and receiving ends. In case the 75Ω E1 unbalanced coaxial cable is used, the grounding jumpers at the receiving end should be disconnected while those at the transmitting end should be short-circuited, provided that the PC is properly grounded. This configuration is the factory default setting and applicable to most situations so that there is usually no need to change it. If there is difficulty in grounding the local PC, you may short-circuit the on-board grounding jumper at the receiving end and use the transmitting end at the opposite terminal for grounding. ④If the receiving end at the opposite terminal is grounded (improper operation), the on-board grounding jumper at the transmitting end must be disconnected. Refer to Table 2-3 for details.

Generally speaking, even in the case of proper grounding at both terminals, only the external layer of the coaxial cable (E1 trunk) at the transmitting end is allowed to be grounded. The grounding of both transmitting and receiving ends may result in a current loop with ground wires, bringing instability to signals. Therefore, such grounding must be strictly avoided.

| Opposite Terminal / Local Terminal | Transmit End | grounded        | grounded        | non-grounded                   | non-grounded    |
|------------------------------------|--------------|-----------------|-----------------|--------------------------------|-----------------|
|                                    | Receive End  | non-grounded    | grounded        | non-grounded                   | grounded        |
| PC grounded                        | Transmit End | short-circuited | disconnected    | short-circuited                | disconnected    |
|                                    | Receive End  | disconnected    | disconnected    | short-circuited                | short-circuited |
| PC not grounded                    | Transmit End | short-circuited | short-circuited | manage to make the PC grounded | short-circuited |
|                                    | Receive End  | short-circuited | disconnected    |                                | short-circuited |

Table 2-3 Configuration of Grounding Jumpers for Use of 75Ω E1 Unbalanced Coaxial Cable

### Step 2: Properly fit the required digital trunk board into the PCI slot on the chassis.

### Step 3: Connect to digital trunks.

In this series, the SHD-120D-CT/PCI, SHD-120D-CT/PCI/EC and SHD-120D-CT/PCI/MAP boards provide common RJ48C connectors, each of which accommodates one E1/T1 and supports direct connection to other boards, the pin layout being shown below in Figure 2-6; the SHD-240D-CT/PCI and SHD-240D-CT/PCI/EC boards provide special RJ48C connectors, each of which accommodates two E1/T1 and supports connection to other boards via conversion, the

pin layout being shown below in Figure 2-7.

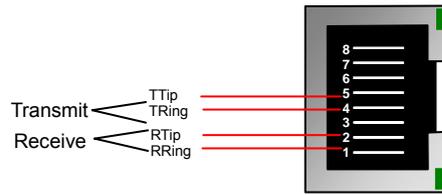


Figure 2-6 Pin Layout for Common RJ48C

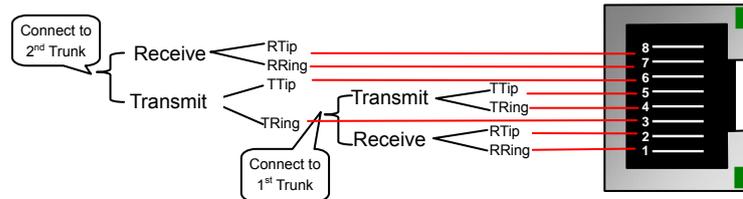


Figure 2-7 Pin Layout for Special RJ48C

When you connect the SHD-240D-CT/PCI or SHD-240D-CT/PCI/EC board to the digital trunk, it is necessary to convert each special RJ48C connector into 2 common RJ48C connectors. Use the 2-way hub for RJ48C for conversion as shown in Figure 2-8: connect the 1<sup>st</sup>, 2<sup>nd</sup>, 4<sup>th</sup>, 5<sup>th</sup> pins of the special 8-pin RJ48C connector to the 1<sup>st</sup>, 2<sup>nd</sup>, 4<sup>th</sup>, 5<sup>th</sup> pins of the first common RJ48C connector, and the 3<sup>rd</sup>, 6<sup>th</sup>, 7<sup>th</sup>, 8<sup>th</sup> pins to the 1<sup>st</sup>, 2<sup>nd</sup>, 4<sup>th</sup>, 5<sup>th</sup> pins of the second common RJ48C connector. You are allowed to construct lines for conversion by yourself, but must follow the order as specified above in pin connection.

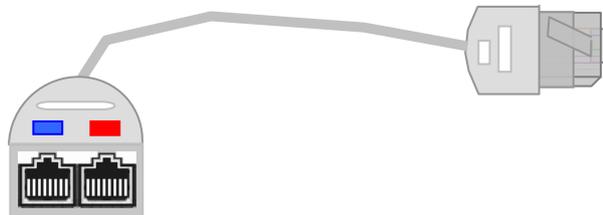


Figure 2-8 2-way Hub for RJ48C

When connecting the SHD-120D-CT/PCI, SHD-120D-CT/PCI/EC or SHD-120D-CT/PCI/MAP board to the digital trunk, if it is necessary to convert the on-board RJ48C connector (including either 1 or 2 E1/T1) to the BNC connector, you can use the RJ48C-to-BNC adapter as shown below (see Figure 2-9).



Figure 2-9 RJ48C-to-BNC Adapter

If you would like to construct lines for conversion by yourself, you should not only make the line match the on-board interface, but also ensure the correct connection of receive

and transmit lines as shown in Figure 2-6.

**Notes:**

- ① Prevent the cross connection of transmit and receive lines. Such mistake can be found out by observing the on-board synchronization indicators. If the indicator is on, that means the receive line is in a normal state; if the indicator is off or flashing, that means the receive line goes abnormal (probably due to the cross connection). However, the state of transmit lines can not be checked via synchronization indicators but should be examined by the opposite terminal.
- ② On-board synchronization indicators start working only after the PC is powered on and the board is successfully initialized.

**Step 4: Connect the bus cable with the H.100 bus on each board.**

Skip this step if there is no need for bus exchange between multiple boards.

**Notes:**

- ① See Figure 2-10 for correct insertion. Do not twist or insert in the opposite direction.

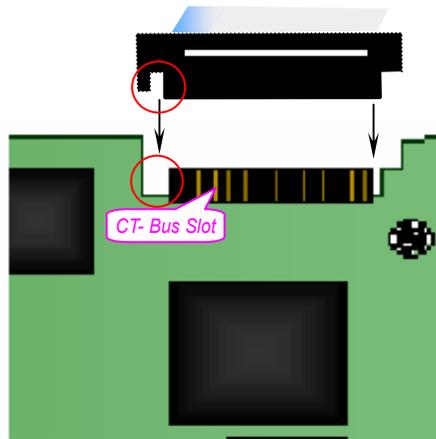


Figure 2-10 Connection of H.100 Bus

- ② There are two clock settings for voice boards: When between-board bus exchange is not required, each board sets its own clock and may not be connected to the bus cable; otherwise, each board must be connected to the bus cable to follow the clock of the cable.
- ③ The bus cable houses a stiff conducting material. Therefore, when it has been shaped, do not bend it repeatedly or violently lest it is broken.

**Step 5: Boot your computer and install the driver.**

Regarding driver installation, refer to *Driver Installation Manual*.

**Step 6: Configure parameters for the digital trunk monitoring board**

Refer to our *SynCTI Programmer's Manual* for details.

## Key Tips:

- As the application system is expected to run for long hours unmanned, 'energy-saving' mode should be turned off for both the CPU and the HD in CMOS or WINDOWS operating system. This is to ensure full-speed operation of the computer, or it may lead to a drop in performance or unexpected errors after running for some time.
- A chassis installed with voice boards must be grounded for safety reasons, according to standard industry requirements. A simple way is earthing with the third pin on the plug. No or improper grounding may cause instability in operation as well as decrease in lightning resistance.

# Appendix A Technical Specifications

## Dimensions

310×115mm<sup>2</sup> (excluding L-bracket)

## Weight

≈ 250g

## Environment

Operating temperature: 0 °C—55 °C

Storage temperature: -20 °C—85 °C

Humidity: 8%—90% non-condensing

Storage humidity: 8%—90% non-condensing

## Input/output Interface

E1 interface: Compliant with G.703, including  
75Ω unbalanced interface and  
120Ω balanced interface

T1 interface: DSX-1 and CSU line build-outs  
available for different extents of  
signal losses, including 100Ω  
and 110Ω balanced interfaces

## Audio Specifications

CODEC: CCITT A/μ-Law 64kbps,

Distortion: ≤3%

Frequency response: 300-3400Hz (±3dB)

Signal-to-noise ratio: ≥38dB

Echo suppression: ≥40dB

## Maximum System Capacity

Up to 8 digital trunk boards concurrently  
per system; up to 120 or 240 channels per

board

## Enhanced Echo Canceller

SHD-120D-CT/PCI/EC: 128ms

SHD-240D-CT/PCI/EC: 128ms

## Power Requirements

Maximum power consumption: ≤15W

## Signaling

SS1: Compliant with DL and MFC  
standards stipulated in GF002-9002

SS7: Compliant with related provisions  
stated in Q771-Q795

DSS1: Compliant with Q.933

## Audio Encoding & Decoding

16Bit PCM 128kbps

8Bit PCM 64kbps

A-Law 64kbps

μ-Law 64kbps

IMA ADPCM 32kbps

GSM 13.6kbps

MP3 8kbps

## Safety

Lightning Resistance: Level 4

Certification: FCC; CE

## Appendix B Technical/sales Support

Thank you for choosing Synway. Please contact us should you have any inquiry regarding our products. We shall do our best to help you.

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